

Fig. 1A

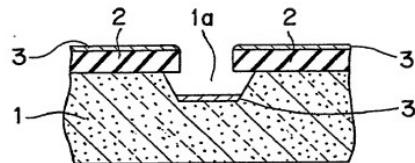


Fig. 1B

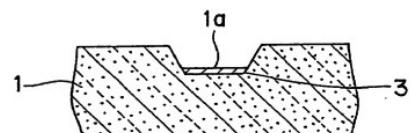


Fig. 1C

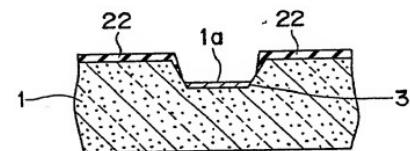


Fig. 1D

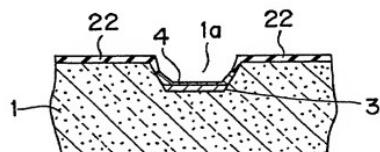


Fig. 1E

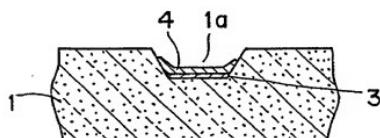


Fig. 2F

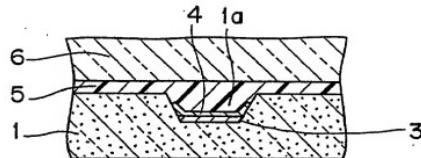


Fig. 2G

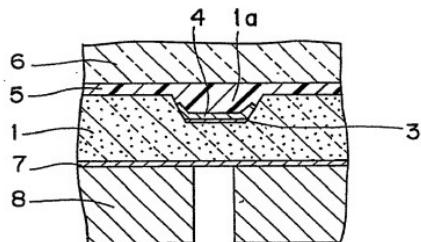


Fig. 2H

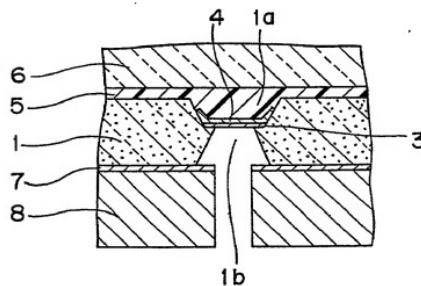


Fig. 3I

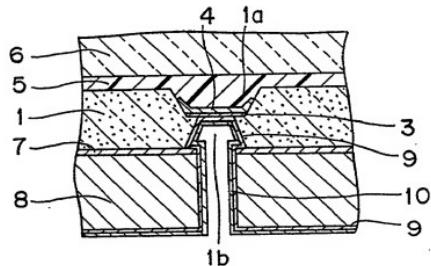


Fig. 3J

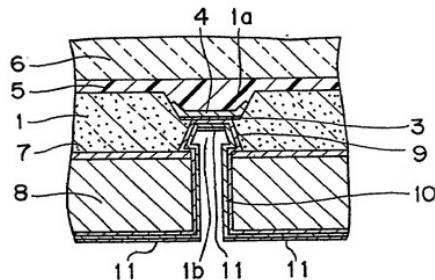


Fig. 3K

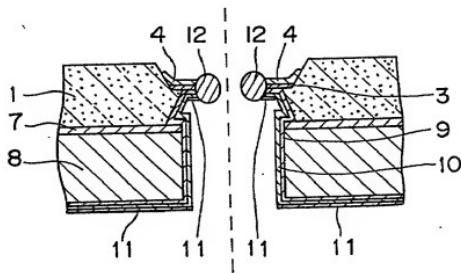


Fig. 4A

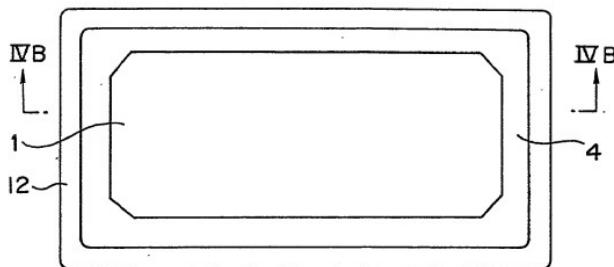


Fig. 4B

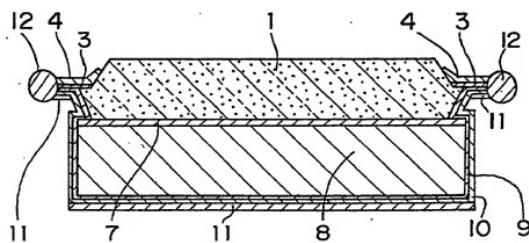


Fig. 5A

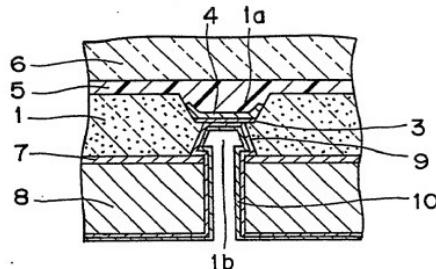


Fig. 5B

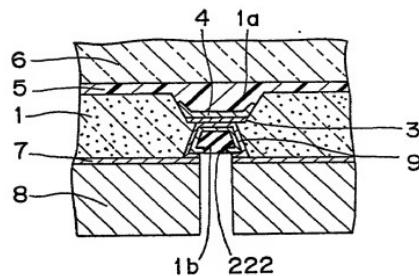
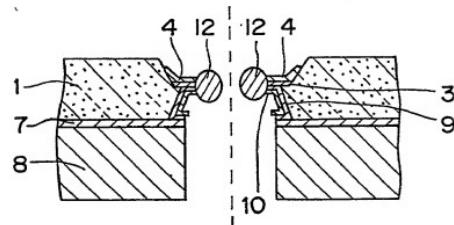
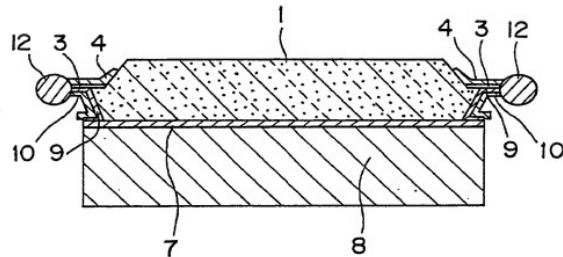


Fig. 5C

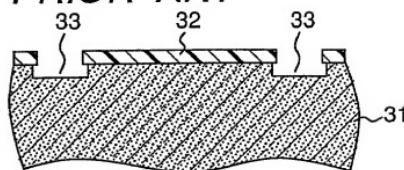


Title: Semiconductor Device  
Inventors: Kosaki et al.  
Atty Docket No.: 401440  
Leydig, Voit & Mayer, Ltd.  
202-737-6770

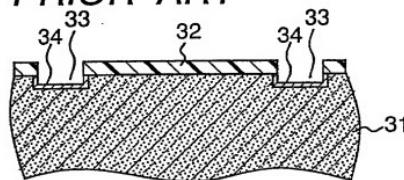
*Fig. 6*



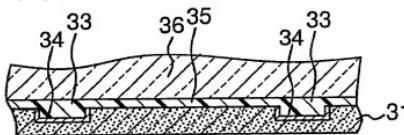
*Fig.7A* *PRIOR ART*



*Fig.7B* *PRIOR ART*



*Fig.7C* *PRIOR ART*



*Fig.7D* *PRIOR ART*

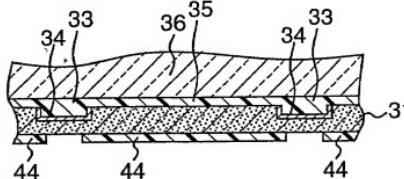


Fig.8E PRIOR ART

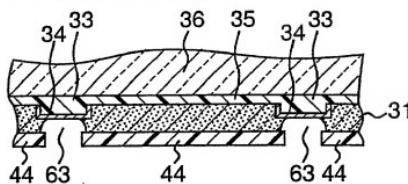


Fig.8F PRIOR ART

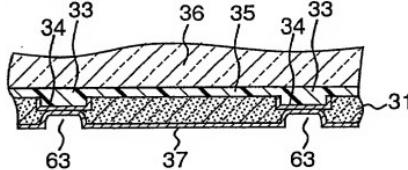


Fig.8G PRIOR ART

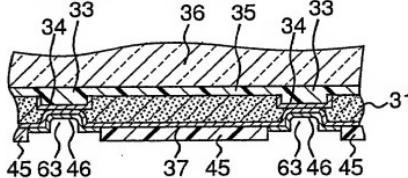
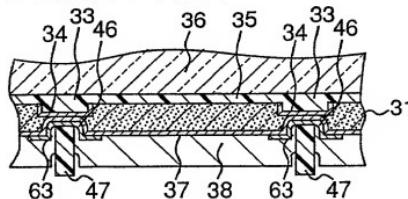
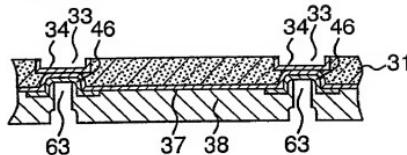


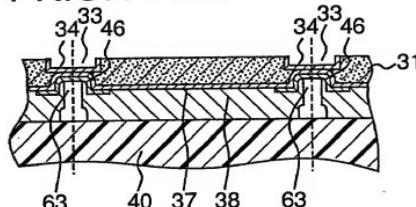
Fig.8H PRIOR ART



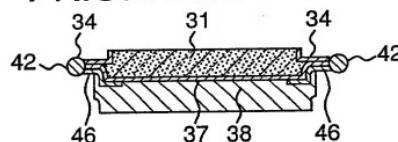
*Fig.9I PRIOR ART*



*Fig.9J PRIOR ART*



*Fig.9K PRIOR ART*



*Fig.9L PRIOR ART*

